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Sheet 1 of 1

FORM 1449* INFORMATION DISCLOSURE STATEMENT IN AN APPLICATION (Use several sheets if necessary)	Docket Number: 40873.1244USWO	Application Number: 10/553,860
	Applicant: NANAI et al.	
	Filing Date: October 20, 2005	Group Art Unit: 2818

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO
/DG/	JP 11-251259	9/17/1999	Japan			Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/DG/ 05/12/2008		Avouris et al. "Carbon Nanotube Transistors and Logic Circuits," PHYSICA B, October 2002, vol. 323, Issues 1-4, pp. 6-14

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EXAMINER	/David Goodwin/	DATE CONSIDERED	05/12/2008

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.

*Substitute Disclosure Statement Form (PTO-1449) Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE